

FIRST YEAR

Digital Techniques II	COE105
Prerequisite : COE103	(3-2-0-2)

Canonical & Standard Forms digital Logic Gates. Karnaugh Maps: AND OR & implementation, don't care condition. Adders Arithmetic operation: Subtractions, half & full adders & subtracts, binary parallel adders. Codes Conversion: Even and odd parity logic, decoders encoders compactor, multiplexes & Demultiplexers. Sequential Logic: Flip Flops(RS, T, D, JK) Master slave FF. Counters and Shift Registers: Asynchronous, synchronous, up-down synchronous, counter applications, shift register, kinds of shift register and applications of shift register. Data Conversion Circuits: Digital to analog convertor , Digital to analog convertor specifications , types of digital to analog convertor and applications of digital to analog convertor. Analog to digital convertor, analog to digital convertor specifications, types of analog to digital convertor, and applications of analog to digital convertor. **Practical part:** *Karnaugh Map. Arithmetic operations. Flip-Flops, Multiplexing and de-multiplexing. Counters. registers.*